

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Kyle K. Kirby

Serial No.: 10/673,692

Filed: September 29, 2003

For: METHOD FOR CREATING
ELECTRICAL PATHWAYS FOR
SEMICONDUCTOR DEVICE
STRUCTURES USING LASER
MACHINING PROCESSES

Confirmation No.: 4168

Examiner: M. Estrada

Group Art Unit: 2823

Attorney Docket No.: 2269-5665US
(02-1291.00/US)

VIA ELECTRONIC FILING
October 16, 2007

AMENDMENT

Mail Stop Amendment
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

Sir:

The following amendments and remarks are filed in response to the Examiner's remarks in the Office Action mailed July 26, 2007, the three-month shortened statutory period for response to which expires on October 26, 2007.

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 9 of this paper.